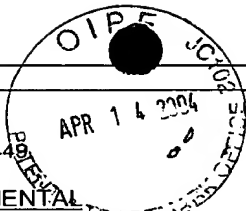


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FORM PTO-1449

THIRD SUPPLEMENTAL
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FIRST NAMED INVENTOR
Michael Gottlieb Jensen

FILING DATE
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U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
RLE	AA1	4,068,303	01/1978	Morita	—	—	—
RLE	AB1	4,814,975	03/1989	Hirosawa <i>et al.</i>	—	—	—
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FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
	AL1						Yes No
	AM1						Yes No
	AN1						Yes No
	AO1						Yes No
	AP1						Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

RLE	AR	1	Turley, J., "LSI's TinyRisc Core Shrinks Code Size: Code-Compaction Technique Squeezes MIPS Instructions Into 16 Bits," Microprocessor Report, Microdesign Resources, October 28, 1996, pp. 40-43.
RLE	AS	1	Sweetman, D., <i>See MIPS Run</i> , Morgan Kaufmann Publishers, Inc., ISBN 1-55860-410-3, 1999, pp. vii-xiv and 423-425.
RLE	AT	1	<i>User's Manual V_R4121TM 64/32-Bit Microprocessor</i> , NEC Corporation, 1998, pp. 1-19 and 103-131.

EXAMINER <u>Richard Ellis</u>	DATE CONSIDERED <u>5/18/2004</u>
<p>EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.</p>	